

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional Application of:

Katsutoshi IZUMI; Motoi NAKAO;
Yoshiaki OHBAYASHI; Keiji MINE;
Seisaku HIRAI; Fumihiko JOBE; and
Tomoyuki TANAKA

Serial No.: Not yet assigned

Filed: March 18, 2004

Group Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

P.T.O. Confirmation No.: Unknown

For: MANUFACTURING APPARATUS FOR BURIED INSULATING LAYER-TYPE
SEMICONDUCTOR SILICON CARBIDE SUBSTRATE

INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 18, 2004

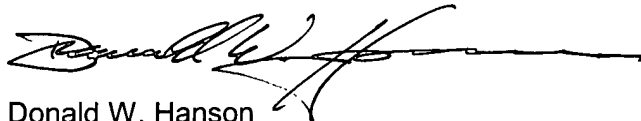
Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 10/351,385.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449

INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 021740A	Serial No. Filed Concurrently
	Applicant(s): Katsutoshi IZUMI; Motoi NAKAO; Yoshiaki OHBAYASHI; Keiji MINE; Seisaku HIRAI; Fumihiko JOBE; and Tomoyuki TANAKA	
	Filing Date: March 18, 2004	Group Art Unit: Not Yet Assigned

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,880,491	Soref et al	3/9/99	257	190	
_____	AB						
_____	AC						
_____	AD						

FOREIGN PATENT DOCUMENTS

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_____	AK				
_____	AL				

OTHER DOCUMENTS

_____	AM	
_____	AN	

Examiner	Date Considered
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